IMPLICIT PROPAGATION OF DIRECTLY ADDRESSED GRIDS IN LBM – PARCFD'2021

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Abstract. Lattice Boltzmann methods are well suited to highly parallel computational fluid dynamics simulations due to their separability into a perfectly parallel collision step and a propagation step that only communicates within a local neighborhood. The implementation of the propagation step bounds both the maximum possible bandwidth-limited performance and places restrictions on the memory layout and usage of vector instructions. This contribution continues the work on implicit propagation pattern started by the A-A pattern [1] and its SSS formulation [2] to introduce a revert- and paddingless Periodic Shift (PS) pattern. Extensive benchmark results for SSS and PS on Intel and AMD CPUs including a Intel Xeon Phi processor using AVX2 and AVX-512 as well as GPUs using CUDA are provided.

1 IMPLICIT PROPAGATION

Implicit propagation is based on manipulating the SFC used for mapping spatial cell locations to directly addressed memory location. Such manipulation depends on a neighborhood property and usage of a Structure-of-Arrays memory layout for population data.

Definition 1.1 (Location invariance of neighborhood distance). Let $x, y \in C$ be a pair of locations in cuboid C. The one-dimensional distance w.r.t. SFC $m_c : C \to \mathbb{N}_0$ is given by

$$\delta: C \times C \to \mathbb{Z}, (x, y) \mapsto m_c(x) - m_c(y).$$

This distance is called *location invariant* iff

$$\forall \xi \in \mathbb{Z}^d \ \forall x, y \in \{x \in C | x + \xi \in C\} : \delta(x, x + \xi) = \delta(y, y + \xi).$$

This invariance of the one-dimensional or *in-memory* distance between all well-defined spatial neighbor locations is the essential property employed for implicit propagation. Curves for which this requirement holds enable streaming of all populations along their respective discrete velocity directions by translation of the starting point.

Definition 1.2 (Implicit Propagation). Let C be a cuboid with memory bijection m_c fulfilling Definition 1.1, $\xi \in \mathbb{Z}^d$ a discrete velocity and t the current time. The memory access function

 $p_t: \mathbb{Z} \to \mathbb{R}$

returns the current population values for all $x \in m_c(C)$ at time t and dummy values for $x \in \mathbb{Z} \setminus m_c(C)$. Propagation of a population at $x \in C$ in timestep t to $x + \xi \in C$ at time t + 1 is equivalent to

$$p_{t+1}(m_c(x+\xi)) = p_t(m_c(x)).$$

Due to invariance of the neighborhood distance the memory access function p_{t+1} can be defined as

$$p_{t+1}: x \mapsto p_t(\tilde{m}_c(x))$$
 where $\tilde{m}_c: x \mapsto m_c(x) + \delta(x, x + \xi)$

while being equivalent to propagation along ξ for all $x \in \{x \in C | x + \xi \in C\}$. Note that p_{t+1} is simply a *shifted* view of the original memory function p_t . The propagation is thus performed implicitly.

It can be shown that the Sweep SFC is the only discrete SFC that fullfills the location invariance of neighborhood distances. Shift-Swap-Streaming [2] (SSS) and Periodic Shift (PS) differ in the way that the indexing function is shifted resulting in different memory access patterns and performance results as well as isotropy properties. Existing patterns such as A-A [1] and A-B can also be formulated in this context.



Figure 1: Propagation without data transfer by control structure update in PS on a D2Q9 lattice

2 PERIODIC SHIFT

The novel Periodic Shift pattern eliminates the need for padding and reverted stores present in SSS by viewing the individual population arrays as cyclic. The streaming step then consists only of rotating the arrays by their propagation distances. In this context the implementation challenge is to perform rotation of such cyclic arrays as efficiently as possible. Approaches using pre-computation of pointers to eliminate modulo operations and page table modifications to utilize virtual address translation (enabling the usage of SIMD intrinsics) are discussed and evaluated.

PERIODICSHIFT (D, f_{old})

1 // Rotate the cyclic arrays along the distances given by discrete velocities

2 **for**
$$iPop \in \{1, ..., D, q\}$$

3 $f_{\text{new}}[iPop] = \text{ROTATE}(f_{\text{old}}[iPop], \text{SHIFTOFFSET}(D.c[iPop]))$

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4 return f_{\text{new}}
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3 BENCHMARKS

Detailed single-node CPU and GPU performance evaluations are performed using benchmark implementations based on SIMD intrinsics resp. CUDA and expressionlevel code optimization (CSE). All performance results are related to memory bandwidth measurements and achieve saturation up to 99%. Scalability benchmarks comparing to SWAP [3] use the OpenLB [4] framework which employs PS starting from version 1.4.

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Figure 2: Performance for SSS and PS on Intel Xeon Phi using AVX-512 and OpenMP



Figure 3: Speedup and efficiency for SWAP and PS using OpenLB and OpenMPI